

Notice of References Cited	Application/Control No. 10/649,256	Applicant(s)/Patent Under Reexamination PAPA RAO ET AL.	
	Examiner Thanh T. Nguyen	Art Unit 2813	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-2004/0028882	02-2004	Andricacos et al.	428/209
	C	US-2002/0185671	12-2002	Kim, Si Bum	257/301
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NON-PATENT DOCUMENTS

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	U	Stanley Wolf, Silicon Processing for the VLSI ERA, 1986, Vol. 1, pages 335-373 ^{TN}
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.